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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

On: September 18, 2004

By: Richard D. Fuerle

Signature: 

Date: September 18, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: YOSHIYUKI ANDO

Examiner: Unknown

Serial No.: 10/707,450

Group Art Unit: Unknown

Filed: December 15, 2003

For: INTEGRATED CIRCUITS HAVING  
POST-SILICON ADJUSTMENT CONTROL

The Commissioner of Patents  
and Trademarks  
Alexandria, VA 22313

**PRELIMINARY AMENDMENT**

Introductory Comments

Please amend the above-identified patent application as follows:

09/22/2004 AAD0F01 00000011 10707450

01 FC:2201  
02 FC:2202

86.00 OP  
90.00 OP